

Enhanced Test Condition

PSE Conformance Test Suite

February 12 2025 2:35 PM

Port Count..... 1

Loop Count..... 1

PSE Tested: **Unspecified Type-2 30W**



802.3at Conformance Report

version 5.3.14

Test Mode: **30 Watt PHY** report version 5.3.08

Sifos Interop Index*: 100%

Error Log: None

Chassis ID: **192.168.221.105**

PSA-3000 Ports

TestLoop: 1

	1-1	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v									
Open_Circuit_Det_Voc=	9.88	V	9.88	9.88	9.88	2.8	Pass	30	Pass
Peak_Det_Vvalid=	7.89	V	7.89	7.89	7.9	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.01	V	4.01	4.01	4	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	3.87	V	3.87	3.87	3.9	1	Pass	7.2	Pass
Detection_Slew=	0.001	V/usec	0.001	0.001	0.001	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.4	V	0.4	0.4	0.4	0	Pass	2.8	Pass
Non_802_Discr_?=	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	****	0	0	0	0	Pass	2	Pass
Test: det_i									
Init_Current_Isc=	0.8	mA	0.8	0.8	0.8	0	Pass	5	Pass
Det_Current_Isc=	0.77	mA	0.77	0.77	0.77	0	Pass	5	Pass
Test: det_range									
Rgood_Max=	30	Kohm	30	30	30	26	Pass	32	Pass
Rgood_Min=	16	Kohm	16	16	16	16	Pass	19	Pass
Rmid_det=	30	Kohm	30	30	30	26	Pass	33	Pass
Cgood_Max=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_?=	0	****	0	0	0	0	Pass	0	Pass
Test: det_time									
Backoff_Time_Tdbo=	797	msec	797	797	797	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	797	msec	797	797	797	-1	Pass	1500	Pass
Backoff_Type=	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	346	msec	346	346	346	5	Pass	500	Pass
Total_Det_Time=	346	msec	346	346	346	5	Pass	1000	Pass
Test: det_rsource									

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PSE_Detect_Source=	0	****	0	0	0	0	Pass	1	Pass
Output_Impedance_Zout=	0	Kohm	0	0	0	45	Pass	2000	Pass
Test: class_v									
Vclass_Max=	18.4	V	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vclass_Min=	17.8	V	17.8	17.8	17.8	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.8	V	8.8	8.8	8.8	7	Pass	10	Pass
Mark_Voltage_Min=	8.7	V	8.7	8.7	8.7	7	Pass	10	Pass
Test: class_time									
Class_0_Count=	1	****	1	1	1	1	Pass	2	Pass
Class_Time_Tpdc=	14	msec	14	14	14	6	Pass	75	Pass
Class_4_Count=	2	****	2	2	2	2	Pass	2	Pass
Event1_Tcle1=	13.6	msec	13.6	13.6	13.6	6	Pass	30	Pass
Event2_Tcle2=	15.5	msec	15.5	15.5	15.5	6	Pass	30	Pass
Mark_Tme1=	7.7	msec	7.7	7.7	7.7	6	Pass	12	Pass
Mark_Tme2=	77.5	msec	77.5	77.5	77.5	6	Pass	376	Pass
Mark_Tme1_LowI=	8.8	msec	8.8	8.8	8.8	6	Pass	12	Pass
Test: class_err									
Class_lim=	68	mA	68	68	68	51	Pass	100	Pass
Pwr_Cl_lim=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	****	0	0	0	0	Pass	0	Pass
Treset=	439	msec	439	439	439	15	Pass	10000	Pass
Test: pwrup_time									
Power-On_Trise_c0=	104	usec	104	104	104	15	Pass	50000	Pass
Power-On_Tpon_c0=	62.5	msec	62.5	62.5	62.5	0	Pass	400	Pass
Power-On_Trise_c4=	128	usec	128	128	128	15	Pass	50000	Pass
Power-On_Tpon_c4=	134.8	msec	134.8	134.8	134.8	0	Pass	400	Pass
Test: pwrup_inrush									
Init_Iinrush=	411.63	mA	411.63	411.63	411.6	400	Pass	450	Pass
Max_Iinrush_c0=	416.5	mA	416.5	416.5	416.5	400	Pass	450	Pass
Max_Iinrush_c4=	417.38	mA	417.38	417.38	417.4	400	Pass	450	Pass
Min_Iinrush=	407.25	mA	407.25	407.25	407.3	400	Pass	450	Pass
Tinrush=	62.3	msec	62.3	62.3	62.3	50	Pass	75	Pass
Inrush_45m=	54.3	V	54.3	54.3	54.3	50	Pass	57	Pass

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Inrush_Voltage=	30.5	V	30.5	30.5	30.5	30	Pass	57	Pass
Inrush_Strategy_c0=	0	****	0	0	0	0	Pass	1	Pass
Inrush_Strategy_c4=	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v									
Vport_min=	53.5	V	53.5	53.5	53.5	50	Pass	57	Pass
Vport_max=	54.4	V	54.4	54.4	54.4	50	Pass	57	Pass
Vport_ripple=	121	mVpp	121	121	121	0	Pass	500	Pass
Vport_noise=	24	mVpp	24	24	24	0	Pass	200	Pass
Vtrans_min=	52.7	V	52.7	52.7	52.7	50	Pass	57	Pass
Vtrans_max=	54.5	V	54.5	54.5	54.5	50	Pass	57	Pass
Test: pwron_pwr cap									
Pcon_c0=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c0=	134.1	%	134.1	134.1	134.1	100	Pass	9999	Pass
Pcon_c1=	5.8	W	5.8	5.8	5.8	3.9	Pass	22.7	Pass
Icon_%_c1=	147.6	%	147.6	147.6	147.6	100	Pass	9999	Pass
Pcon_c2=	8.2	W	8.2	8.2	8.2	6.8	Pass	22.7	Pass
Icon_%_c2=	122.7	%	122.7	122.7	122.7	100	Pass	9999	Pass
Pcon_c3=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c3=	134.1	%	134.1	134.1	134.1	100	Pass	9999	Pass
Pcon_c4=	32.5	W	32.5	32.5	32.5	28.7	Pass	38.9	Pass
Icon_%_c4=	111.2	%	111.2	111.2	111.2	100	Pass	9999	Pass
Type-2_Enable=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_Peak_1=	413.3	mA	413.3	413.3	413.3	0	Pass	1750	Pass
Ilim_Min_1=	403.7	mA	403.7	403.7	403.7	400	Pass	1750	Pass
Tlim_1=	62.9	msec	62.9	62.9	62.9	10	Pass	9999	Pass
Vlim_1=	53.4	V	53.4	53.4	53.4	50	Pass	57	Pass
Ilim_Max_1=	468.8	mA	468.8	468.8	468.8	400	Pass	1750	Pass
Ktran_lo_1=	107.4	%	107.4	107.4	107.4	92.4	Pass	115	Pass
Ilim_Peak_2=	689.5	mA	689.5	689.5	689.5	0	Pass	1750	Pass
Ilim_Min_2=	687	mA	687	687	687	683	Pass	1750	Pass
Tlim_2=	62.9	msec	62.9	62.9	62.9	10	Pass	75	Pass
Vlim_2=	53.1	V	53.1	53.1	53.1	50	Pass	57	Pass
Ilim_Max_2=	834	mA	834	834	834	683	Pass	1750	Pass
Ktran_lo_2=	106.2	%	106.2	106.2	106.2	92.4	Pass	115	Pass

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Test: pwrn_overld									
%Ipeak_1=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	53.4	V	53.4	53.4	53.4	50	Pass	57	Pass
Vport_5%DC_1=	53.4	V	53.4	53.4	53.4	50	Pass	57	Pass
%Ipeak_2=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_2=	53.2	V	53.2	53.2	53.2	50	Pass	57	Pass
Vport_5%DC_2=	53.3	V	53.3	53.3	53.3	50	Pass	57	Pass
Test: mps_dc_valid									
Min_Valid_Time_Tmps=	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn									
Min_Valid_I_hold=	8	mA	8	8	8	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	312	msec	312	312	312	300	Pass	400	Pass
Test: pwrn_overld									
Icut_1=	373.5	mA	373.5	373.5	373.5	-1	Pass	1750	Pass
Tcut_1=	62.5	msec	62.5	62.5	62.5	50	Pass	9999	Pass
Isoft_1=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_1=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Icut_2=	644.3	mA	644.3	644.3	644.3	-1	Pass	1750	Pass
Tcut_2=	62.3	msec	62.3	62.3	62.3	10	Pass	9999	Pass
Isoft_2=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrn_time									
Turn-Off_Time_Toff=	59.1	msec	59.1	59.1	59.1	0	Pass	500	Pass
Output_Cap_Cout=	0.0665	uF	0.0665	0.0665	0.07	-1	Pass	0.52	Pass
Output_Load_Rp=	9999	Kohm	9999	9999	9999	45	Pass	50000	Pass
Test: pwrn_v									
Avg_Idle_Voff=	0.3	V	0.3	0.3	0.3	0	Pass	2.8	Pass
Error_Delay_Ted=	2351.6	msec	2351.6	2351.6	2352	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	1.1	V	1.1	1.1	1.1	0	Pass	20.5	Pass
Test Port Model Number:	3202								
Test Port Hardware Version:	11								
Test Port Firmware Version:	4.19 lc19								