Statement of Volatility – Dell Pro Max 14 Premium MA14250

△ CAUTION: A CAUTION indicates either potential damage to hardware or erasure of data and tells you how to avoid the problem.

The Dell Pro Max 14 Premium MA14250 contains both volatile and non-volatile components. Volatile components erase their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following non-volatile components are present on the Dell Pro Max 14 Premium MA14250 system board.

Table 1. List of non-volatile components on system board

Description	Reference designator	Volatility description	User accessible for external data	Remedial action (action necessary to erase data)
Embedded Flash in the embedded controller MEC5407	UE1	448 KB code/Data SRAM 384 KB code/64 KB data optimized for performance	No	Not applicable
Panel EEDID EEPROM	Part of the panel assembly	Non-volatile memory. FHD+ 256 bytes QHD+ 256 bytes	No	Part of the panel assembly
System BIOS	UC2	Non-volatile memory. 64 MB x 1 System BIOS and Video BIOS for basic boot operation, PSA (on board diags), PXE diags.	No	Not applicable
System Memory – LPDDR5 – LPDDR5x	On board DRAM+ UD1/UD2/UD3/UD4	Volatile memory in the OFF state. The system memory size will depend on LPDDR5/LPDDR5x: 32 GB, 64 GB, 128 GB (x32) per package	No	Not applicable
GPU VRAM DGFF Motherboard 1 (MB1)	UV411/UV412/UV413/UV414/UV415/UV416	Volatile memory in the OFF state. 16 GB GDDR6 SGRAM 32 M x 16 Bits x 16 banks x 2-channel/ 364 M x 8 Bits x 16 Banks x 2-channel	No	Not applicable
GPU VRAM DGFF Motherboard 2 (MB2)	UV411/UV412/UV413/UV414	Volatile memory in the OFF state. 16 GB GDDR6 SGRAM	No	Not applicable

System memory SPD EEPROM	Part of memory module assembly	Non-volatile memory. Intel ME firmware for system configuration, security, and protection	No	Not applicable
Hard drive(s)	SSD1	2280 M.2 type SSD (PCIE interface)	Yes	Low level format
Card reader	uSD 7.1 card reader controller F/W UR1	PCIE interface of the embedded Flash memory	No	Not applicable
Touchscreen Embedded Flash	Part of the panel assembly	I2C interface of the embedded Flash memory	No	Part of the panel assembly
Accelerometer+ Gyro LIS2DW12TR	UN1	I2C interface of the embedded Flash memory	No	Not applicable
ACC LIS2DW12TR (CVF)	UN1	I2C interface of the embedded Flash memory	No	Not applicable
TPM 2.0 ST33HTPH2X32AHE4 NPCT760JABYX	UK101	SPI interface of the embedded Flash memory	No	Not applicable
MCU ATSAMD21G17D-MUT	No	USB interface of the embedded Flash memory	No	Not applicable
Synaptics SVP7500B0	UDV22	SPI interface of the embedded Flash memory	No	Not applicable
PD CYPD8229-52LQXIT CYPD8229-52LQXIT	UT301	I2C interface of the embedded Flash memory	No	Not applicable
Hayden Bridge TBT Re-timer	UT1(MB) UT1/UT2/UT8 (R_IO)	I2C interface of the embedded Flash memory	No	Not applicable
SPI Flash (Hayden Bridge)	UT4/UT10	Non-volatile memory. 1.8 V 8 Mbit Flash memory	No	Not applicable
SPI Flash (USH)	No	Non-volatile memory. 128 Mbit (16MB) for CV3 Firmware flash.	No	Not applicable

BCM58202 (USH)	**No**	**1 MB of on-chip scratch memory.** **256 KB on-chip boot ROM**	**No**	**Not applicable**
SPI Flash DGFF MB1/DGFF MB2	**UV410**	**Non-volatile memory** **1.8 V 32 Mbit Flash memory**	**No**	**Not applicable**
SPI Flash (DG2)	**No**	**Non-volatile memory.** **1.8V 64 Mbit Flash memory**	**No**	**Not applicable**
Fingerprint sensor	**Module**	**USB interface of the embedded Flash memory**	**No**	**Not applicable**
Touchpad	**Module**	**I2C interface of the embedded Flash memory**	**No**	**Not applicable**
Camera sensor	**Module**	**I2C interface of the embedded Flash memory**	**No**	**Not applicable**
Digital IMVP9.2 controller	**PUZ001**	**Non-volatile memory.** **255 KB Digital IMVP9.2 controller**	**No**	**Not applicable**

△ CAUTION: All other components on the system board lose data if power is removed from the computer. Primary power loss (unplugging the power cable and removing the battery) destroys all user data on the memory. Secondary power loss (removing the onboard coin-cell battery) destroys system data on the system configuration and time-of-day information.

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