

Enhanced Test Condition

PSE Conformance Test Suite

May 10 2024	3:02 PM
Port Count.....	1
Loop Count.....	1
PSE Tested: Unspecified Type-2 30W	



802.3at Conformance Report

version 5.3.14

Test Mode: **30 Watt PHY** report version 5.3.08

Sifos Interop Index*: **100%**

Error Log: None

Chassis ID: **192.168.221.105**

PSA-3000 Ports

TestLoop: **1**

	1-1	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v									
Open_Circuit_Det_Voc=	5.1	V	5.1	5.1	5.1	2.8	Pass	30	Pass
Peak_Det_Vvalid=	8.85	V	8.85	8.85	8.9	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.77	V	4.77	4.77	4.8	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	4.08	V	4.08	4.08	4.1	1	Pass	7.2	Pass
Detection_Slew=	0.005	V/usec	0.005	0.005	0.005	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	1	edges	1	1	1	1	Pass	9	Pass
Backoff_Voltage=	0.8	V	0.8	0.8	0.8	0	Pass	2.8	Pass
Non_802_Discr_?=	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	1	****	1	1	1	0	Pass	2	Pass
Test: det_i									
Init_Current_Isc=	1.15	mA	1.15	1.15	1.15	0	Pass	5	Pass
Det_Current_Isc=	1.14	mA	1.14	1.14	1.14	0	Pass	5	Pass
Test: det_range									
Rgood_Max=	29	Kohm	29	29	29	26	Pass	32	Pass
Rgood_Min=	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	29	Kohm	29	29	29	26	Pass	33	Pass
Cgood_Max=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_?=	0	****	0	0	0	0	Pass	0	Pass
Test: det_time									
Backoff_Time_Tdbo=	727	msec	727	727	727	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	727	msec	727	727	727	-1	Pass	1500	Pass
Backoff_Type=	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	277	msec	277	277	277	5	Pass	500	Pass
Total_Det_Time=	277	msec	277	277	277	5	Pass	1000	Pass
Test: det_rsource									

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PSE_Detect_Source=	0	****	0	0	0	0	Pass	1	Pass
Output_Impedance_Zout=	0	Kohm	0	0	0	45	Pass	2000	Pass
Test: class_v									
Vclass_Max=	19.4	V	19.4	19.4	19.4	15.5	Pass	20.5	Pass
Vclass_Min=	19.2	V	19.2	19.2	19.2	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	9.8	V	9.8	9.8	9.8	7	Pass	10	Pass
Mark_Voltage_Min=	9.6	V	9.6	9.6	9.6	7	Pass	10	Pass
Test: class_time									
Class_0_Count=	1	****	1	1	1	1	Pass	2	Pass
Class_Time_Tpdc=	9.5	msec	9.5	9.5	9.5	6	Pass	75	Pass
Class_4_Count=	2	****	2	2	2	2	Pass	2	Pass
Event1_Tcle1=	12.7	msec	12.7	12.7	12.7	6	Pass	30	Pass
Event2_Tcle2=	16	msec	16	16	16	6	Pass	30	Pass
Mark_Tme1=	9.6	msec	9.6	9.6	9.6	6	Pass	12	Pass
Mark_Tme2=	9.6	msec	9.6	9.6	9.6	6	Pass	376	Pass
Mark_Tme1_LowI=	9.6	msec	9.6	9.6	9.6	6	Pass	12	Pass
Test: class_err									
Class_lim=	71	mA	71	71	71	51	Pass	100	Pass
Pwr_Cl_lim=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	****	0	0	0	0	Pass	0	Pass
Treset=	451	msec	451	451	451	15	Pass	10000	Pass
Test: pwrup_time									
Power-On_Trise_c0=	62	usec	62	62	62	15	Pass	50000	Pass
Power-On_Tpon_c0=	9.8	msec	9.8	9.8	9.8	0	Pass	400	Pass
Power-On_Trise_c4=	69	usec	69	69	69	15	Pass	50000	Pass
Power-On_Tpon_c4=	48.8	msec	48.8	48.8	48.8	0	Pass	400	Pass
Test: pwrup_inrush									
Init_Iinrush=	422.38	mA	422.38	422.38	422.4	400	Pass	450	Pass
Max_Iinrush_c0=	420.25	mA	420.25	420.25	420.3	400	Pass	450	Pass
Max_Iinrush_c4=	420.13	mA	420.13	420.13	420.1	400	Pass	450	Pass
Min_Iinrush=	419	mA	419	419	419	400	Pass	450	Pass
Tinrush=	60	msec	60	60	60	50	Pass	75	Pass
Inrush_45m=	56	V	56	56	56	50	Pass	57	Pass

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Inrush_Voltage=	30.7	V	30.7	30.7	30.7	30	Pass	57	Pass
Inrush_Strategy_c0=	1	****	1	1	1	0	Pass	1	Pass
Inrush_Strategy_c4=	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v									
Vport_min=	55.4	V	55.4	55.4	55.4	50	Pass	57	Pass
Vport_max=	56	V	56	56	56	50	Pass	57	Pass
Vport_ripple=	22	mVpp	22	22	22	0	Pass	500	Pass
Vport_noise=	85	mVpp	85	85	85	0	Pass	200	Pass
Vtrans_min=	55.2	V	55.2	55.2	55.2	50	Pass	57	Pass
Vtrans_max=	56.1	V	56.1	56.1	56.1	50	Pass	57	Pass
Test: pwron_pwr cap									
Pcon_c0=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c0=	134.9	%	134.9	134.9	134.9	100	Pass	9999	Pass
Pcon_c1=	18.4	W	18.4	18.4	18.4	3.9	Pass	22.7	Pass
Icon_%_c1=	472.8	%	472.8	472.8	472.8	100	Pass	9999	Pass
Pcon_c2=	18.4	W	18.4	18.4	18.4	6.8	Pass	22.7	Pass
Icon_%_c2=	276.3	%	276.3	276.3	276.3	100	Pass	9999	Pass
Pcon_c3=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c3=	134.9	%	134.9	134.9	134.9	100	Pass	9999	Pass
Pcon_c4=	34	W	34	34	34	28.7	Pass	38.9	Pass
Icon_%_c4=	117.8	%	117.8	117.8	117.8	100	Pass	9999	Pass
Type-2_Enable=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_Peak_1=	267.8	mA	267.8	267.8	267.8	0	Pass	1750	Pass
Ilim_Min_1=	403.6	mA	403.6	403.6	403.6	400	Pass	1750	Pass
Tlim_1=	60.5	msec	60.5	60.5	60.5	10	Pass	9999	Pass
Vlim_1=	55.5	V	55.5	55.5	55.5	50	Pass	57	Pass
Ilim_Max_1=	420.3	mA	420.3	420.3	420.3	400	Pass	1750	Pass
Ktran_lo_1=	110.8	%	110.8	110.8	110.8	92.4	Pass	115	Pass
Ilim_Peak_2=	320.5	mA	320.5	320.5	320.5	0	Pass	1750	Pass
Ilim_Min_2=	687	mA	687	687	687	683	Pass	1750	Pass
Tlim_2=	60.5	msec	60.5	60.5	60.5	10	Pass	75	Pass
Vlim_2=	55.1	V	55.1	55.1	55.1	50	Pass	57	Pass
Ilim_Max_2=	813.8	mA	813.8	813.8	813.8	683	Pass	1750	Pass
Ktran_lo_2=	109.8	%	109.8	109.8	109.8	92.4	Pass	115	Pass

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Test: pwrn_overld									
%Ipeak_1=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	55.7	V	55.7	55.7	55.7	50	Pass	57	Pass
Vport_5%DC_1=	55.7	V	55.7	55.7	55.7	50	Pass	57	Pass
%Ipeak_2=	113.6	%	113.6	113.6	113.6	100	Pass	125	Pass
Vport_Ipeak_2=	55.2	V	55.2	55.2	55.2	50	Pass	57	Pass
Vport_5%DC_2=	55.2	V	55.2	55.2	55.2	50	Pass	57	Pass
Test: mps_dc_valid									
Min_Valid_Time_Tmps=	15	msec	15	15	15	1	Pass	60	Pass
Duty_Cycle_tol=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn									
Min_Valid_I_hold=	8	mA	8	8	8	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	351	msec	351	351	351	300	Pass	400	Pass
Test: pwrn_overld									
Icut_1=	369.3	mA	369.3	369.3	369.3	-1	Pass	1750	Pass
Tcut_1=	60.2	msec	60.2	60.2	60.2	50	Pass	9999	Pass
Isoft_1=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_1=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Icut_2=	640.5	mA	640.5	640.5	640.5	-1	Pass	1750	Pass
Tcut_2=	60.3	msec	60.3	60.3	60.3	10	Pass	9999	Pass
Isoft_2=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrn_time									
Turn-Off_Time_Toff=	33.3	msec	33.3	33.3	33.3	0	Pass	500	Pass
Output_Cap_Cout=	0.0691	uF	0.0691	0.0691	0.07	-1	Pass	0.52	Pass
Output_Load_Rp=	347	Kohm	347	347	347	45	Pass	50000	Pass
Test: pwrn_v									
Avg_Idle_Voff=	0.7	V	0.7	0.7	0.7	0	Pass	2.8	Pass
Error_Delay_Ted=	984.4	msec	984.4	984.4	984	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	1.1	V	1.1	1.1	1.1	0	Pass	20.5	Pass
Test Port Model Number:	3202								
Test Port Hardware Version:	11								
Test Port Firmware Version:	4.19 lc19								